

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	volatile same nonvolatile same ((self adj test) BIST BISR BISTR) same external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 18:49
L5	45	volatile same nonvolatile same ((self adj test) BIST BISR BISTR)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:15
L6	131	(volatile same nonvolatile same module) and ((self adj test) BIST BISR BISTR)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 18:57
L7	6	volatile same nonvolatile same ((self adj test) BIST BISR BISTR) same external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:26
L8	131	(volatile same nonvolatile same (MCM module)) and ((self adj test) BIST BISR BISTR)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 18:58
L9	131	(volatile same nonvolatile same (MCM module)) and ((self adj test) BIST BISR BISTR MBIST)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 18:59
L10	99	(volatile same nonvolatile same (MCM module)) and ((self adj test) BIST BISR BISTR MBIST) and external\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:09
L11	18	(volatile same nonvolatile same (MCM module)) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with (test testing tester tested))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:21
L12	18	l11 not l7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:10

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L13	45	I5 not I12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:16
L14	39	I5 not I7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:18
L15	908547	I5 not I7 and MCM or module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:18
L16	11	I5 not I7 and (MCM or module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:18
L17	0	((volatile same nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with (test testing tester tested))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:23
L18	0	((volatile same nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with ((self adj test) test testing tester tested))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:23
L19	0	((volatile same nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with ((self adj test) test testing tester tested))).clm.	US-PGPUB	OR	ON	2007/10/14 19:25
L20	1	((volatile DRAM) same nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with ((self adj test) test testing tester tested))).clm.	US-PGPUB	OR	ON	2007/10/14 19:28
L21	10	(RAM DRAM SRAM volatile) same nonvolatile same ((self adj test) BIST BISR BISTR) same external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:28
L22	10	(RAM DRAM SRAM CAM volatile) same nonvolatile same ((self adj test) BIST BISR BISTR) same external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/14 19:28

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L23	1	((((volatile SRAM RAM CAM DRAM) same nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with ((self adj test) test testing tester tested))).clm.	US-PGPUB	OR	ON	2007/10/14 19:29
S1	50	((("5662768" "5731945" "5807791" "5943254" "6180426" "5266912" "5400003" "5502333" "5612657" "5798282" "5946545" "5761516" "5893153" "6140149" "5454160" "5677569" "5933712" "6190425" "6836021" "7016227" "5829016" "5973952" "5999440" "5450364" "4468759" "5890196" "4783782" "5995428" "6182183" "6029224" "5661339" "5708297" "5731633" "5751553" "5805520" "6049975" "6081008" "6091145" "6232659" "5696031" "4577293" "5758056" "4783732" "4831519" "4835729" "4852048" "4916657" "4983533" "5588152" "5590345").pn.) and DRAM	USPAT	OR	OFF	2007/10/14 18:44
S2	1	"6971051"	USPAT	OR	OFF	2006/11/03 20:48
S3	4	"6671836"	USPAT	OR	OFF	2006/11/03 20:49
S4	1	"6971051"	USPAT	OR	OFF	2007/03/21 13:46
S5	0	"20010061893"	US-PGPUB	OR	OFF	2007/08/22 10:36
S6	0	"20010061893"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37
S7	0	"200100161893"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37
S8	2	"20010016893"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37

EAST Search History

S9	2	"20010016893" and sense	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37
S10	0	"20010016893" and sense and amplifier and driver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37
S11	1	"20010016893" and sense and amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/08/22 10:37

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Set	Items	Description
S1	87669	VOLATILE(1W)(STOR??? OR MEMOR???) OR RAM OR DRAM OR SRAM OR SDRAM OR RDRAM OR SLDRAM OR SGRAM OR DRDRAM
S2	319059	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR (NON())VOLATILE OR NONVOLATILE(1W)(STOR??? OR MEMORY)
S3	15247	SELFTEST OR SELF()TEST OR BIST OR MBIST OR LBIST
S4	833	(DISCONNECT???? OR DISENGAG? OR DISABL? OR BLOCK??? OR STOP???? OR HALT??? OR PROHIBIT??? OR SUPPRESS??? OR PREVENT? OR BAR? ? OR BARRED OR BARRING)(5N)(SIGNAL? ? OR INPUT???? OR ACCESS???) (5N)(EXTERNAL? OR EXTERIOR OR OUTSIDE OR OUTER)
S5	0	S1 AND S2 AND S3 AND S4
S6	128	S1 AND S2 AND S3
S7	2	S3 AND S4
S8	1	S1 AND S2 AND S4
S9	7	S1:S2 AND S4
S10	9	S7:S9
S11	9	RD (unique items)
S12	61	S1(20N)S2(20N)S3
S13	38	RD (unique items)
S14	36	S13 NOT ((S14 OR PY=2004:2007))
S15	5276	AU=(BRAUN, J? OR BRAUN J?)
S16	0	S4 AND S15

File 348:EUROPEAN PATENTS 1978-2007/ 200737

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File 349:PCT FULLTEXT 1979-2007/UB=20070913UT=20070906

(c) 2007 WIPO/Thomson

Set	Items	Description
S1	170549	VOLATILE(1W)(STOR??? OR MEMOR???) OR RAM OR DRAM OR SRAM OR SDRAM OR RDRAM OR SLDRAM OR SGRAM OR DRDRAM
S2	211866	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR (NON())VOLATILE OR NONVOLATILE)(1W)(STOR??? OR MEMORY)
S3	6021	SELFTEST??? OR SELF()TEST??? OR BIST OR MBIST OR LBIST
S4	8381	(DISCONNECT???? OR DISENGAG? OR DISABL? OR BLOCK??? OR STOP???? OR HALT??? OR PROHIBIT??? OR SUPPRESS??? OR PREVENT? OR BAR? ? OR BARRED OR BARRING)(5N)(SIGNAL? ? OR INPUT???? OR ACCESS???) (5N)(EXTERNAL? OR EXTERIOR OR OUTSIDE OR OUTER)
S5	2	S1(50N)S2(50N)S3(50N)S4
S6	6	S1(100N)S2(100N)S3(100N)S4
S7	12	S3(30N)S4
S8	18	S3(50N)S4
S9	139847	(DISCONNECT???? OR DISENGAG? OR DISABL? OR BLOCK??? OR STOP???? OR HALT??? OR PROHIBIT??? OR SUPPRESS??? OR PREVENT? OR BAR? ? OR BARRED OR BARRING)(10N)(EXTERNAL? OR EXTERIOR OR OUTSIDE OR OUTER)
S10	17	S1(100N)S2(100N)S3(100N)S9
S11	34	S5:S8 OR S10
S12	23	S11 AND PY=1978:2003
S13	16	S11 AND (AC=US OR AC=US/PR) AND AY=1978:2003
S14	24	S12:S13
S15	99922	S1(20N)S2
S16	244	S15(100N)S4
S17	56	S1:S2/TI,AB AND S16
S18	56	S17 NOT S14
S19	1147	S4(10N)(CHIP OR MICROCHIP OR IC OR INTEGRATED())CIRCUIT OR - MEMORY)
S20	108	S15(100N)S19
S21	40	S1:S2/TI,AB AND S20
S22	32	S21 AND PY=1978:2003
S23	12	S21 AND (AC=US OR AC=US/PR) AND AY=1978:2003
S24	35	S22:S23
S25	35	IDPAT (sorted in duplicate/non-duplicate order)

File 347:JAPIO Dec 1976-2007/Mar(Updated 070809)

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File 350:Derwent WPIX 1963-2007/UD=200756

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Set	Items	Description
S1	201732	VOLATILE(1W)(STOR??? OR MEMOR???) OR RAM OR DRAM OR SRAM OR SDRAM OR RDRAM OR SLDRAM OR SGRAM OR DRDRAM
S2	154124	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR (NON())VOLATILE OR NONVOLATILE)(1W)(STOR??? OR MEMORY)
S3	4785	SELFTEST??? OR SELF()TEST??? OR BIST OR MBIST OR LBIST
S4	7624	(DISCONNECT???? OR DISENGAG? OR DISABL? OR BLOCK??? OR STOP???? OR HALT??? OR PROHIBIT??? OR SUPPRESS??? OR PREVENT? OR BAR? ? OR BARRED OR BARRING)(5N)(SIGNAL? ? OR INPUT????)(5N)(EXTERNAL? OR EXTERIOR OR OUTSIDE OR OUTER)
S5	10	S3(30N)S4
S6	0	S1 AND S2 AND S5
S7	0	S1 AND S2 AND S3 AND S4
S8	136	S1 AND S2 AND S4
S9	127	S1(10N)S2 AND S4
S10	5	S1:S2 AND S3 AND S4
S11	9	MEMORY AND S3 AND S4
S12	1	PN=US 20050005218
S13	356	TEST??? (30N)S4
S14	4	S1 AND S3 AND S13
S15	12	S10:S11 OR S14
S16	232839	(DISCONNECT???? OR DISENGAG? OR DISABL? OR BLOCK??? OR STOP???? OR HALT??? OR PROHIBIT??? OR SUPPRESS??? OR PREVENT? OR BAR? ? OR BARRED OR BARRING)(10N)(EXTERNAL? OR EXTERIOR OR OUTSIDE OR OUTER)
S17	3	S1 AND S2 AND S3 AND S16
S18	15	S15 OR S17

INTERFERENCE SEARCHED

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L23	1	((((volatile SRAM RAM CAM DRAM) same.nonvolatile) and (MCM module) and ((self adj test) BIST BISR BISTR MBIST) and ((external\$4 outside) with ((self adj test) test testing tester tested))).clm.	US-PGPUB	OR	ON	2007/10/14 19:29

PALM Intranet

Application
Number

IDS Flag Clearance for Application 10822529



Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
WIDS	2004-04-12	14	Y <input checked="" type="checkbox"/>	2007-05-09 00:00:00.0	CR #232884
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